

Figure 1 Monoline Serial Interface

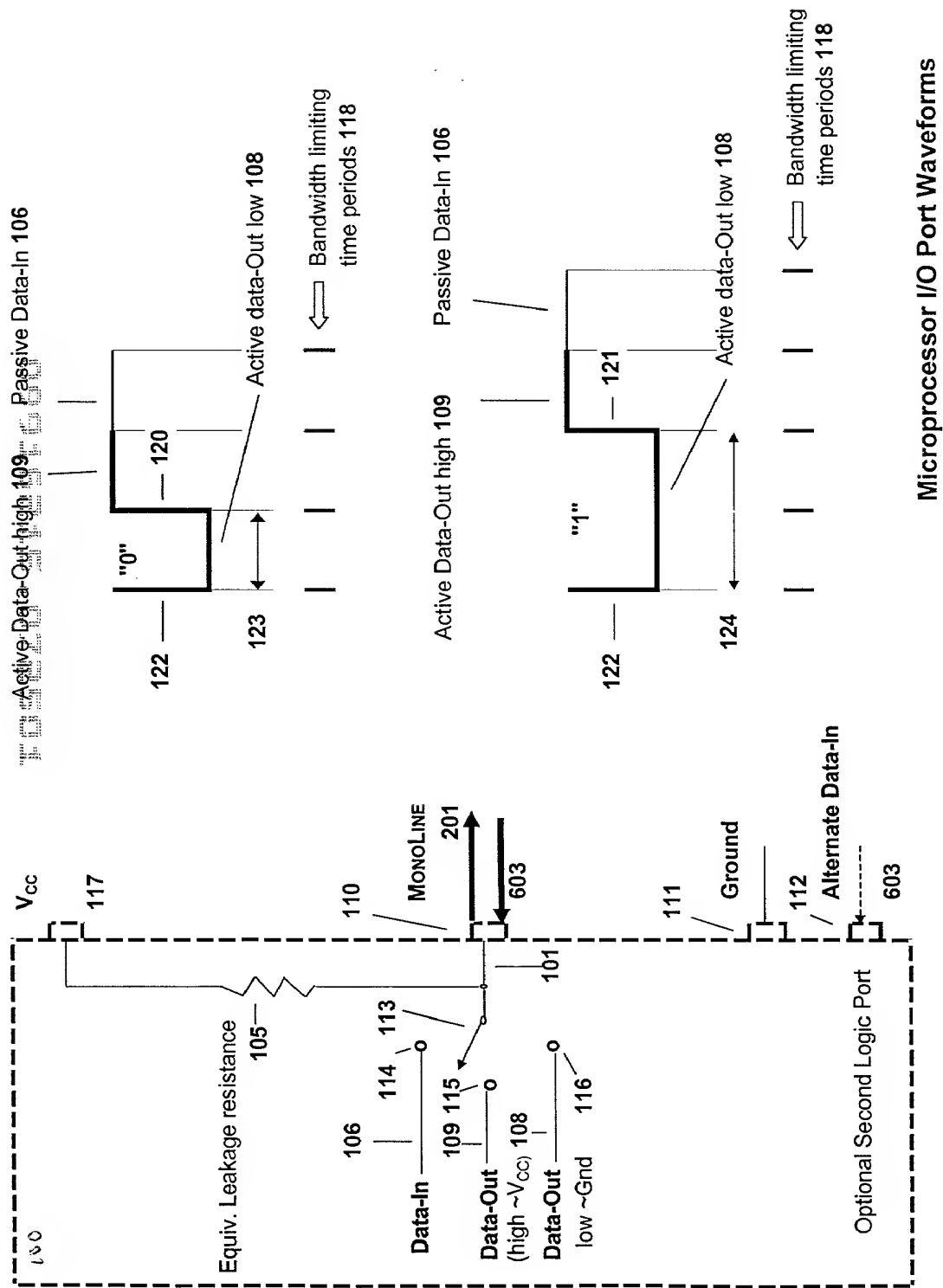


Figure 2

Figure 3

FIG. 4

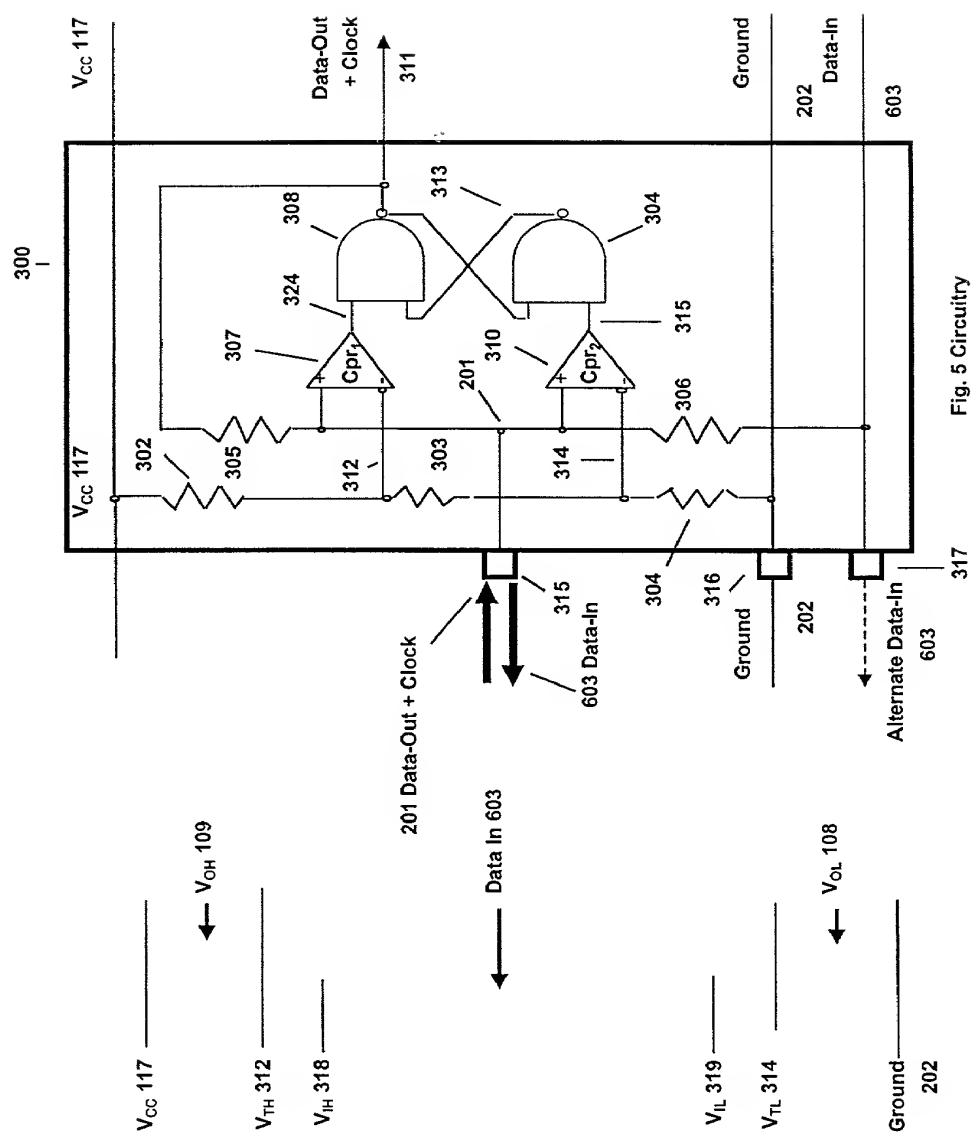


Fig. 4 Signal Levels

Fig. 5 Circuitry

Data Direction Multiplexer

Figure 6

"0" Data-Out bit

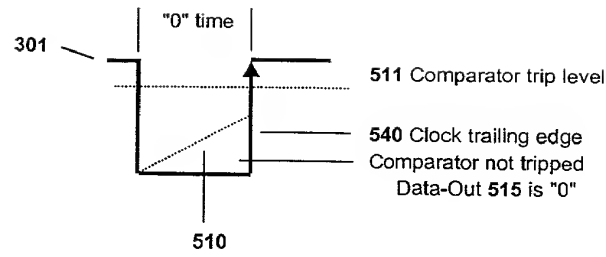
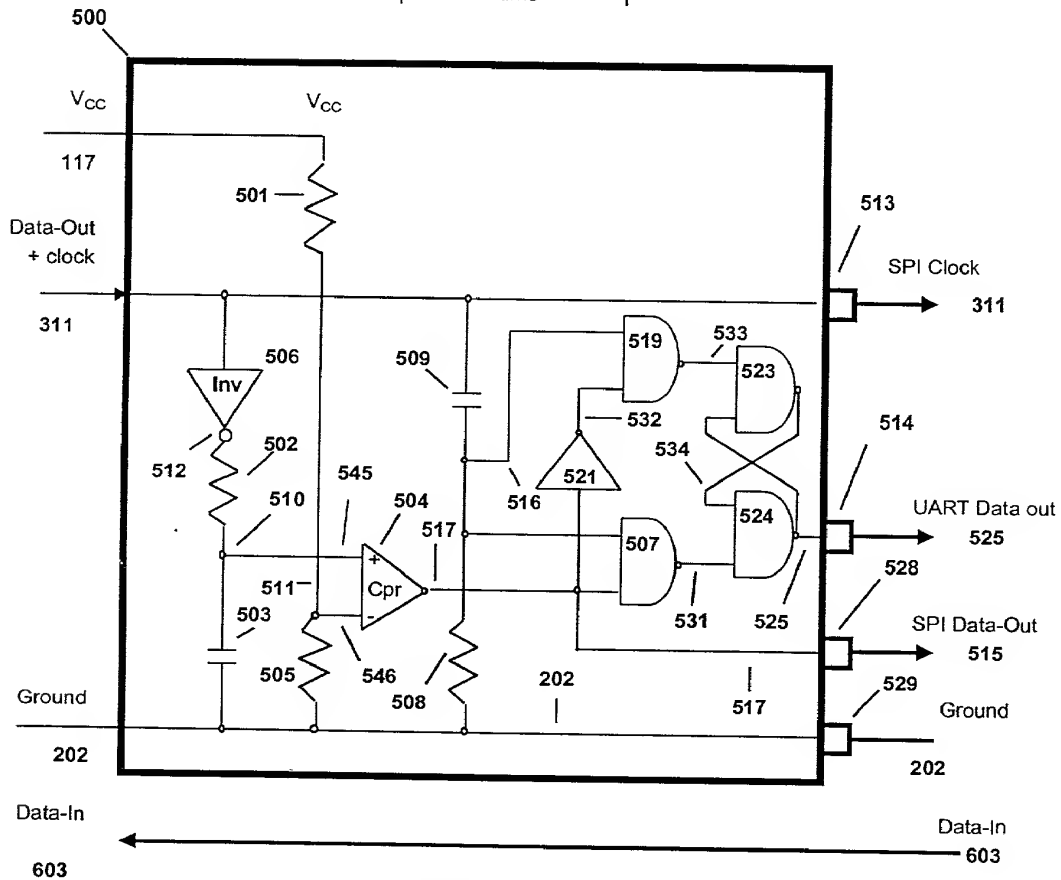
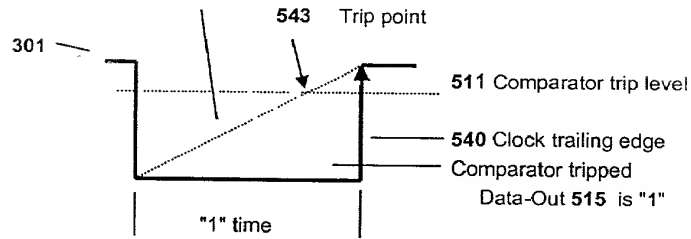
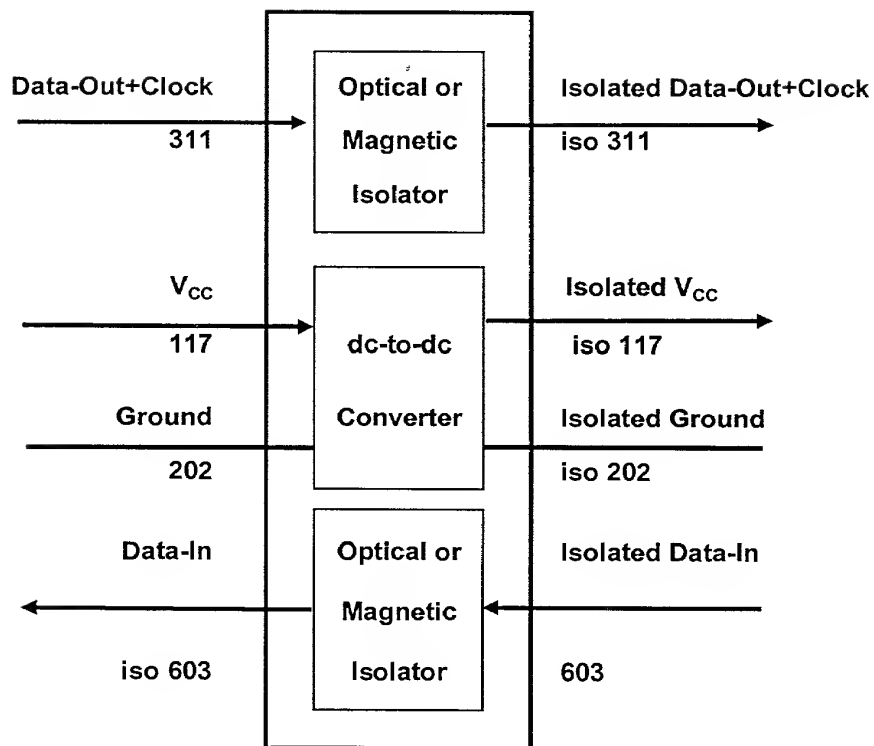


Figure 7

"1" Data-Out bit



Data-Out Extractor Circuit
Figure 8



Optional Electrical Isolation

Figure 9

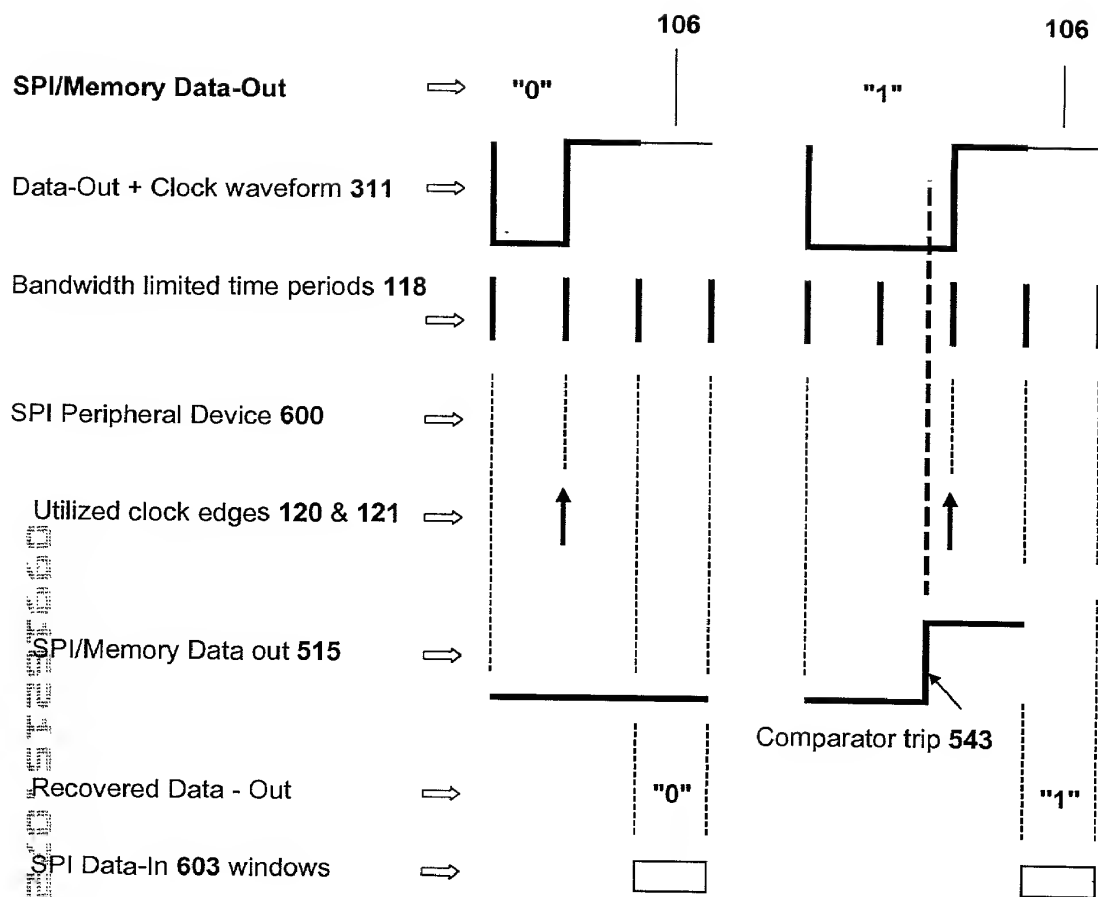


Figure 10 SPI Data

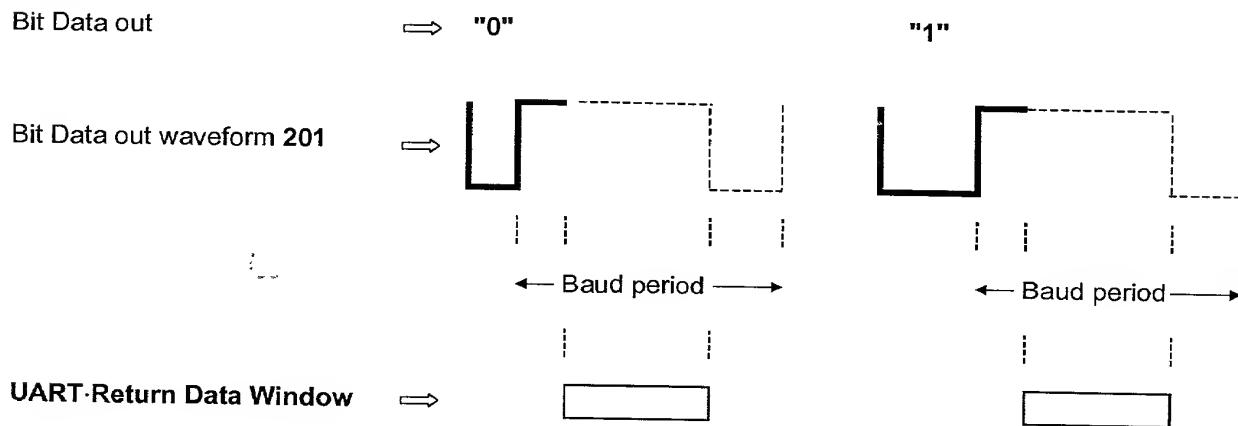


Figure 11 UART Data